

Form PTO-1449
(Rev. 8-83)

U.S. Department of Commerce
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Attorney Docket No. 0756-2045

Serial No. Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Applicant: Kenji ITOH et al.

Group: 1762

Filing Date: October 5, 1999

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
<i>MSD</i>	5,340,621	8/23/94	Matsumoto et al.			
	5,217,761	6/8/93	Okada et al.			
	5,102,523	4/7/92	Beisswenger et al.			
<i>MSD</i>	5,449,410	<i>9/95</i> 4/24/95	Chang et al.			
	5,294,292	3/15/94	Yamashita et al.			
	5,336,326 ←	8/9/94	Karner et al.			
	5,413,673	5/9/95	Fujimoto			
	5,266,116 ←	11/30/93	Fujioka et al.			
	5,203,924	4/20/93	Mitani et al.			
	5,022,979	6/11/91	Hijikata et al.			
	4,991,542	2/12/91	Kohmura et al.			
	4,920,917 ←	5/1/90	Nakatani et al.			
	4,910,041	3/20/90	Yanagihara et al.			
	4,663,828	5/12/87	Hanak			
	4,578,559	3/25/86	Hijikata et al.			
	4,534,816	8/13/85	Chen et al.			
	5,104,685 ←	4/14/92	Takahashi et al.			
	5,013,583	5/7/91	Yamaura et al.			
	5,302,424 ←	4/12/94	Murai et al.			
<i>MSD</i>	4,755,426	7/5/88	Kokai et al.			

Examiner

Date Considered

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
<i>MLP</i>	05-345980	12/27/93	Japan			Abstract	
<i>MLP</i>	63-317536	12/26/88	Japan			Abstract	
<i>MLP</i>	63-150926	6/23/88	Japan <i>Other</i>			Abstract	
			<i>Other</i>				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>MLP</i>		Wolf, Silicon Processing for the VLSI Era, Vol. 1, Lattice Press, 1986, pages 172-173, 544, 570-571. <i>no mention</i>

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7/17/00

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